

Figure 1

Measured Overall Efficiency vs. V_{bus} at Different Loads
($V_{in}=16V$; 1MHz; With Drive Loss)

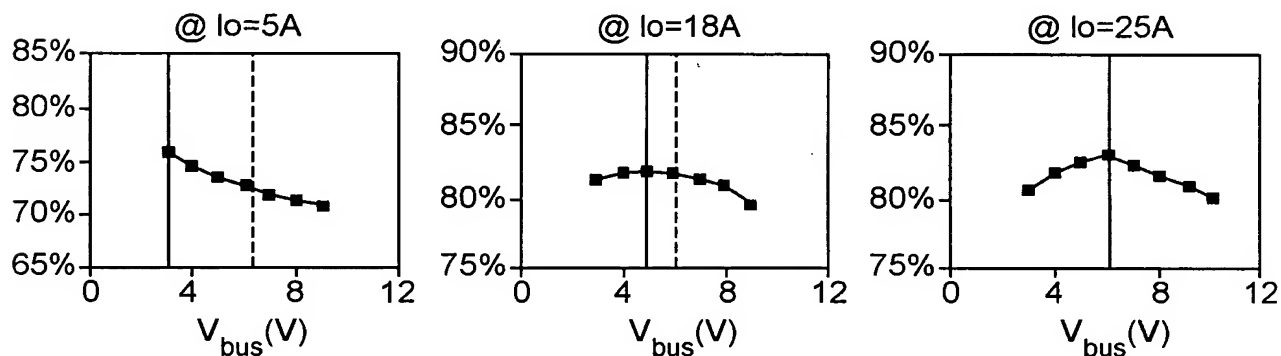


Figure 2

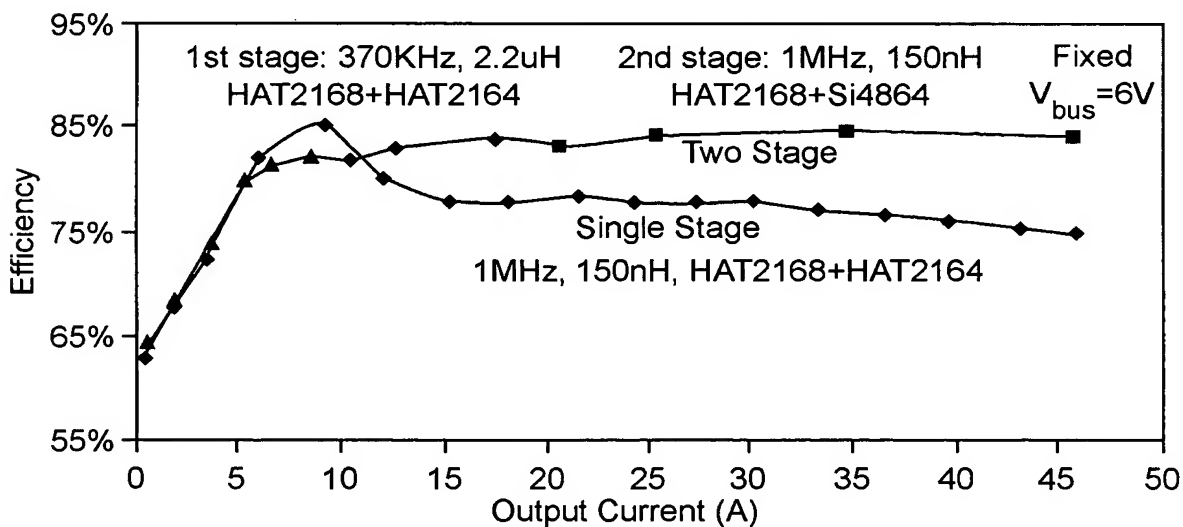


Figure 3A

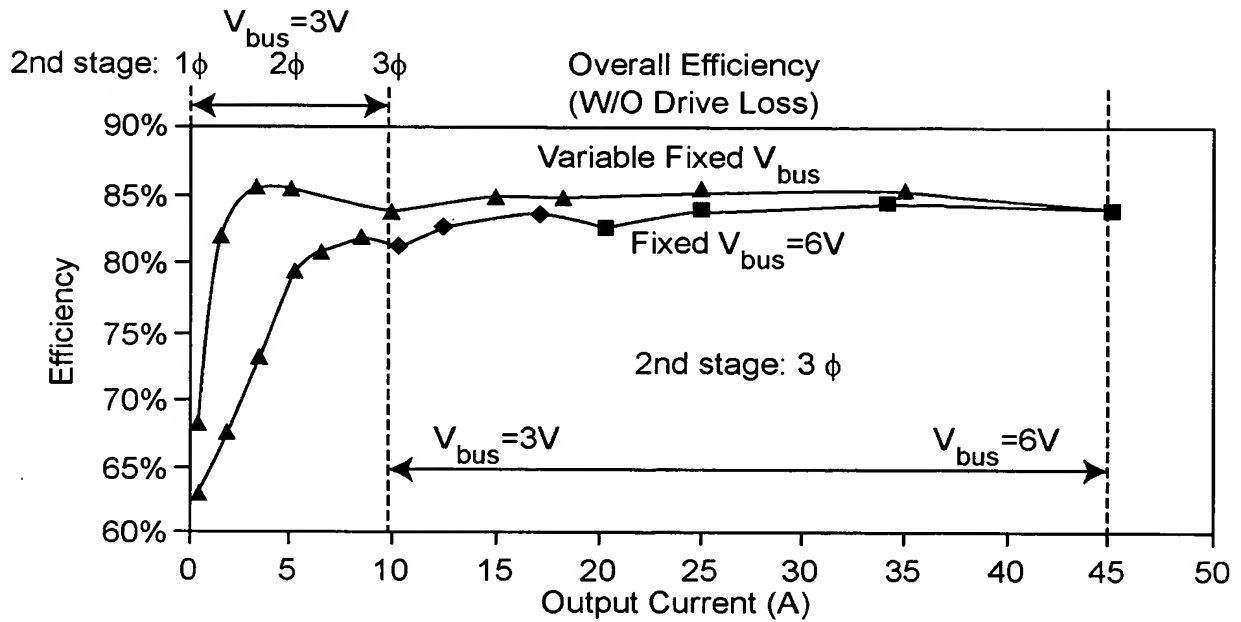


Figure 3B

Global System Power States and Transitions

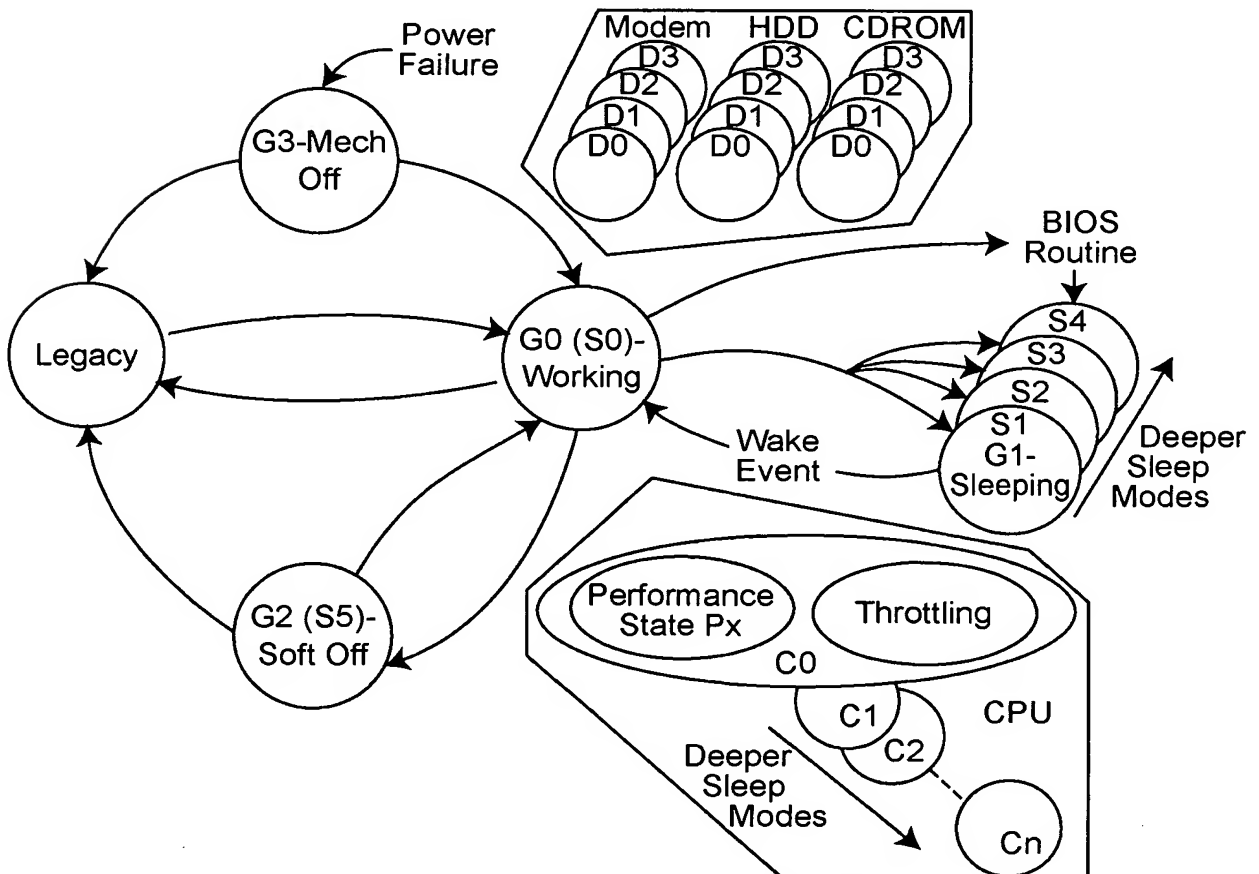
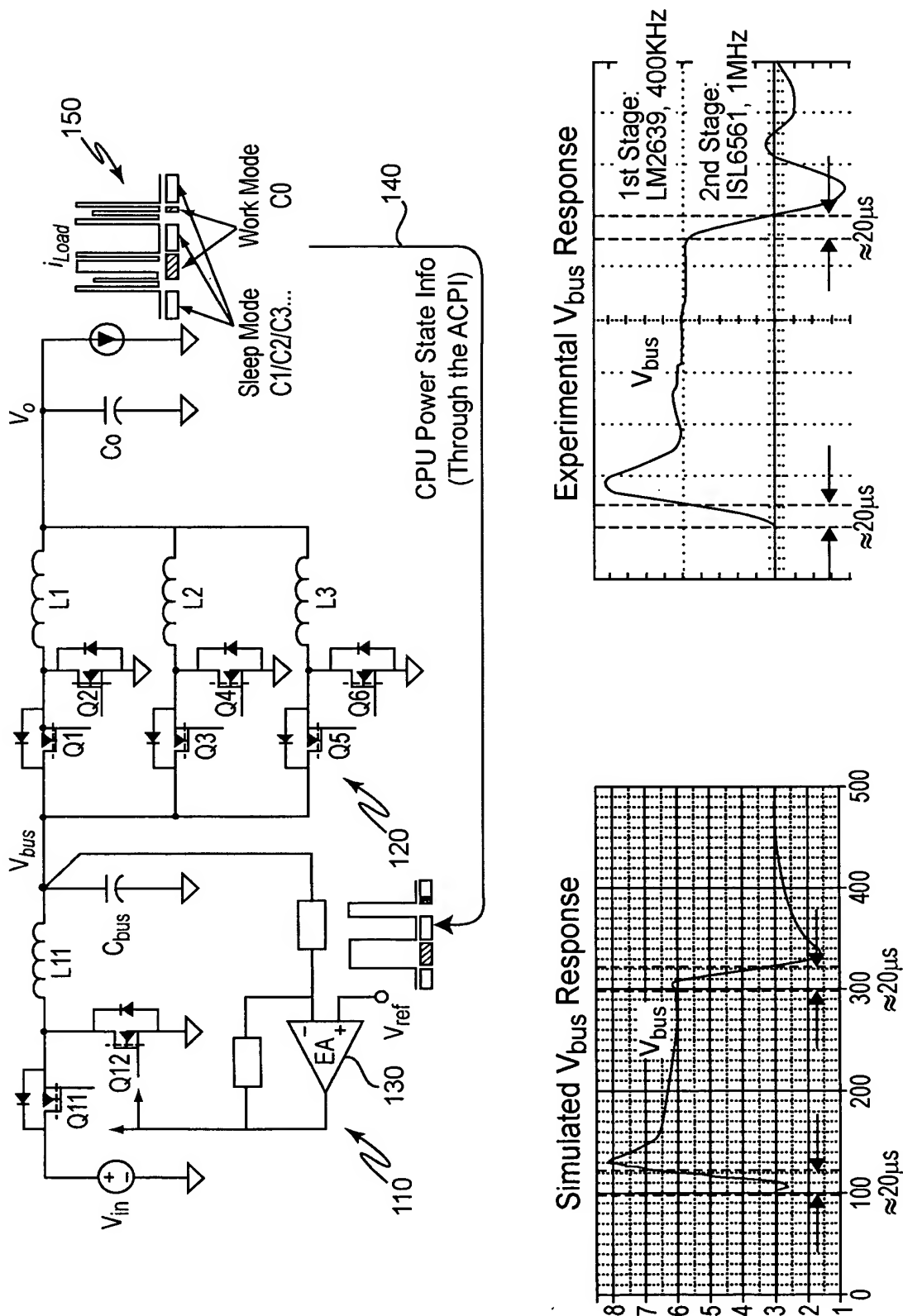


Figure 4



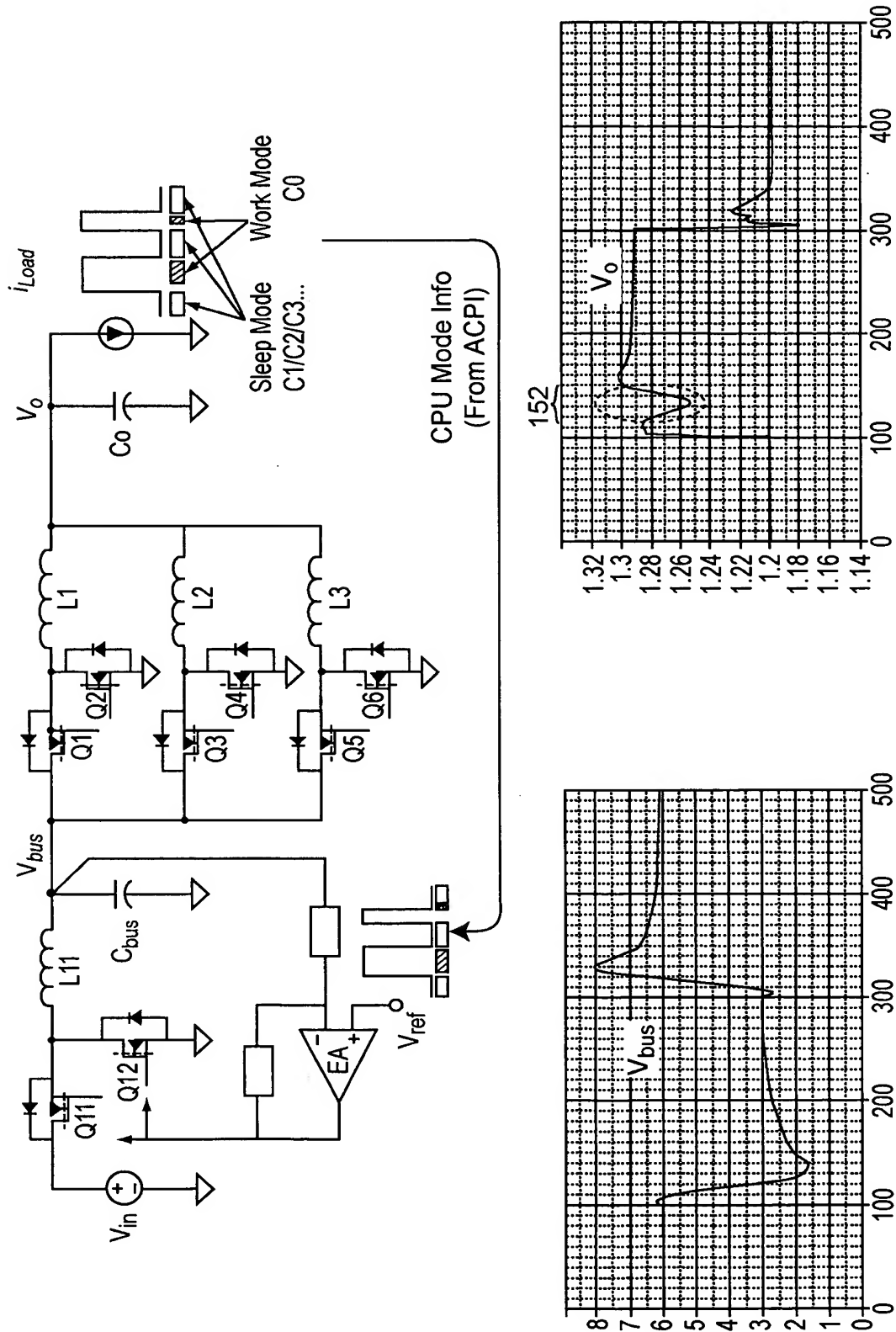


Figure 6



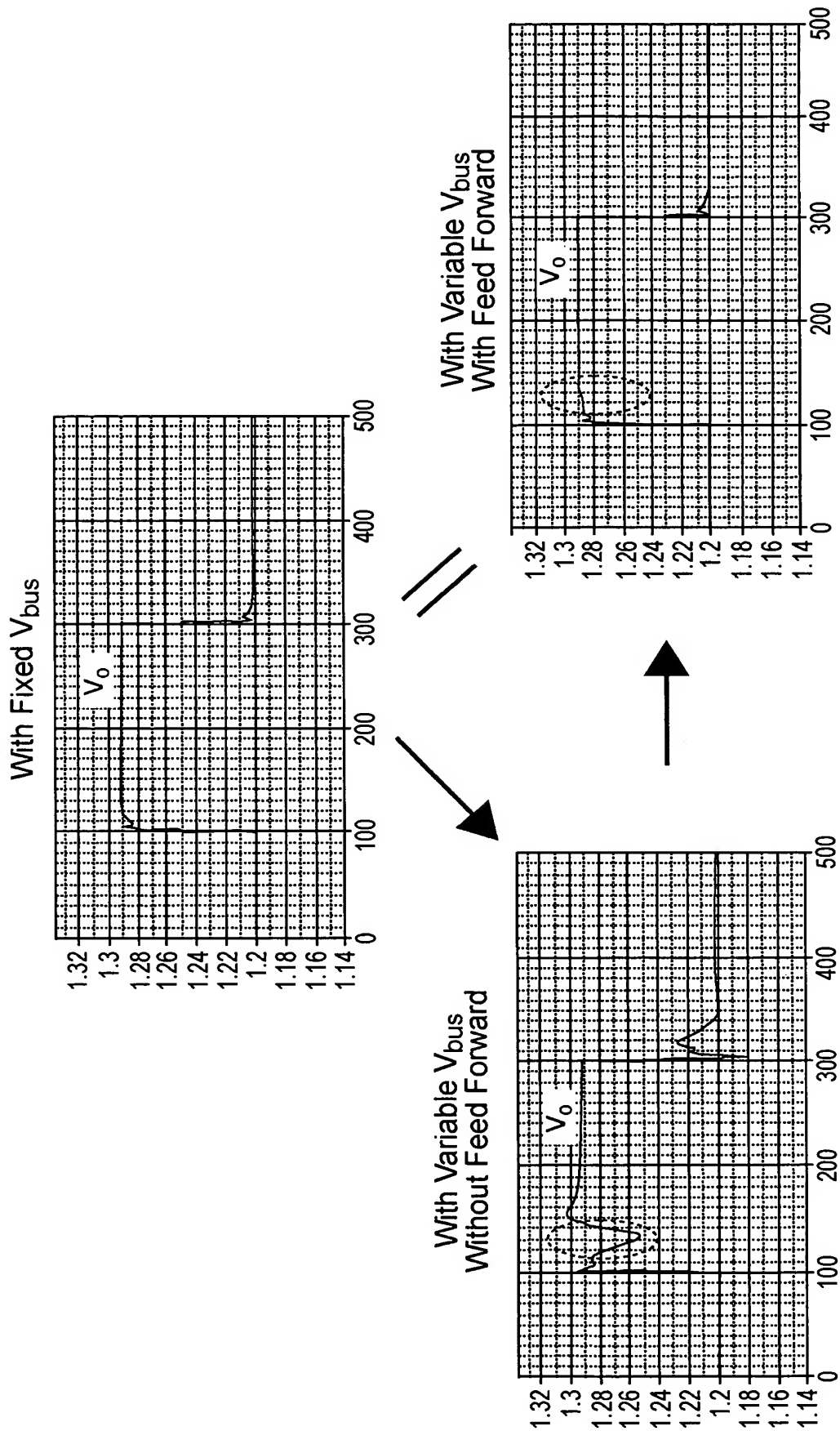


Figure 9

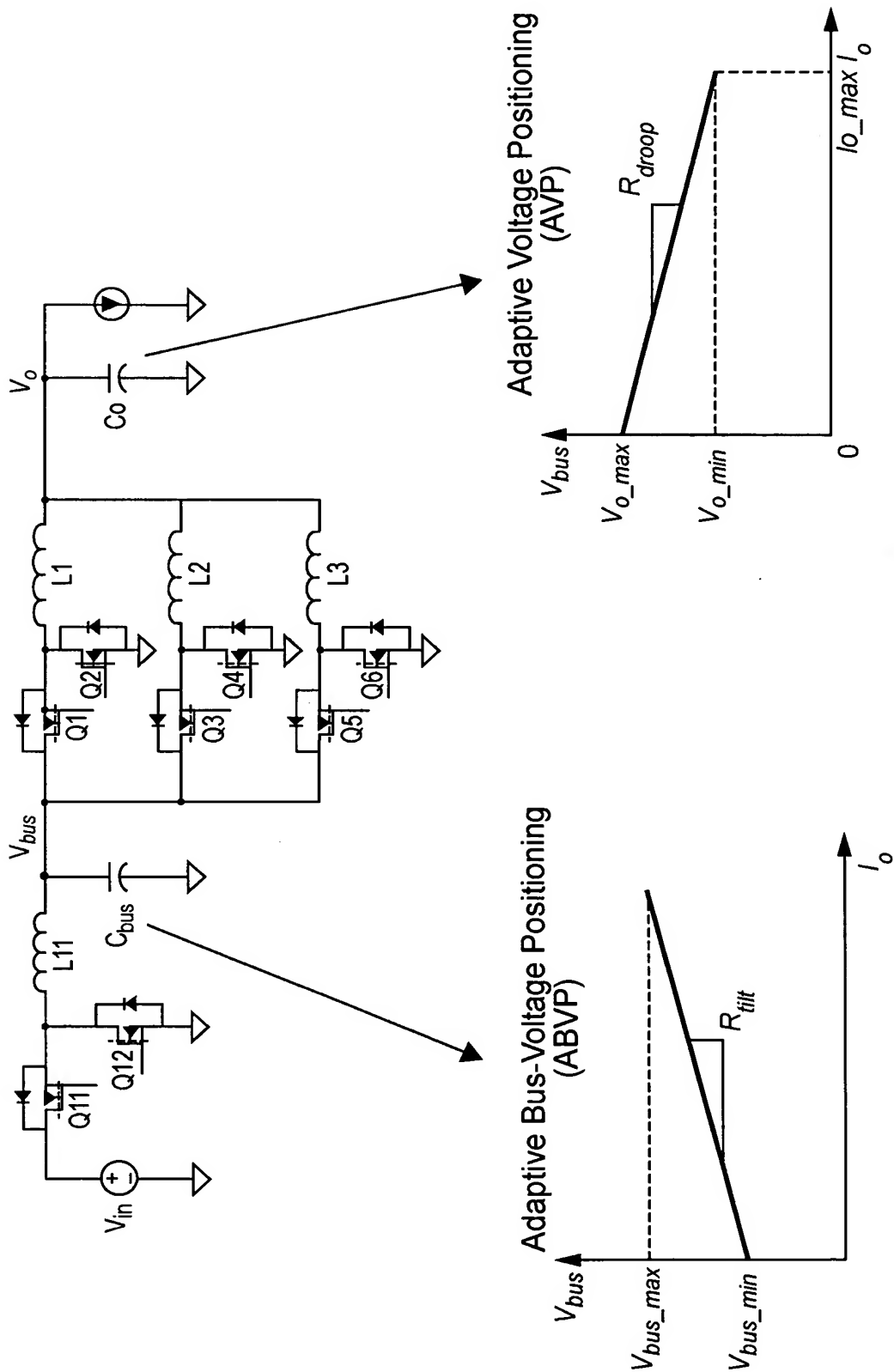


Figure 10A

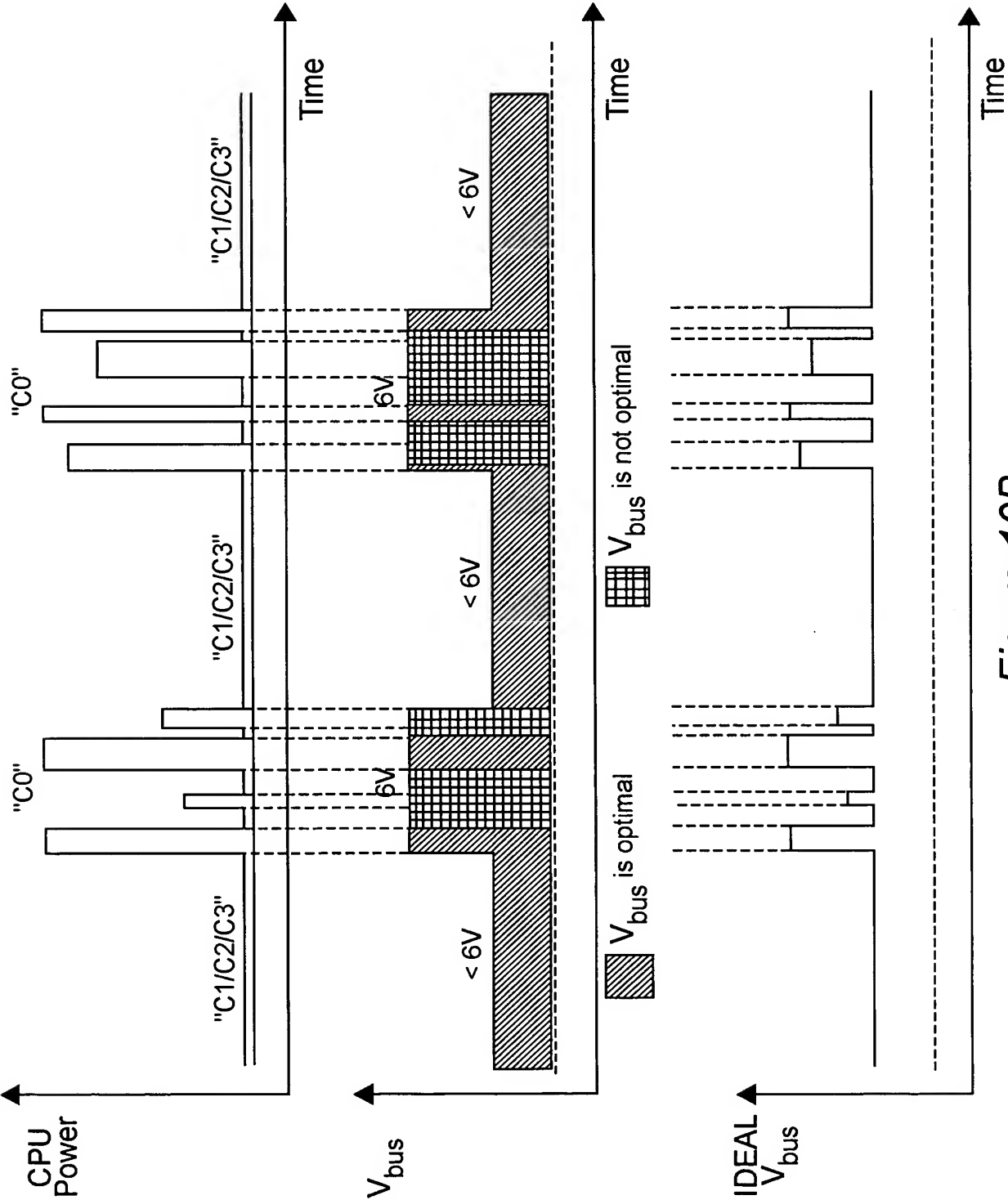


Figure 10B

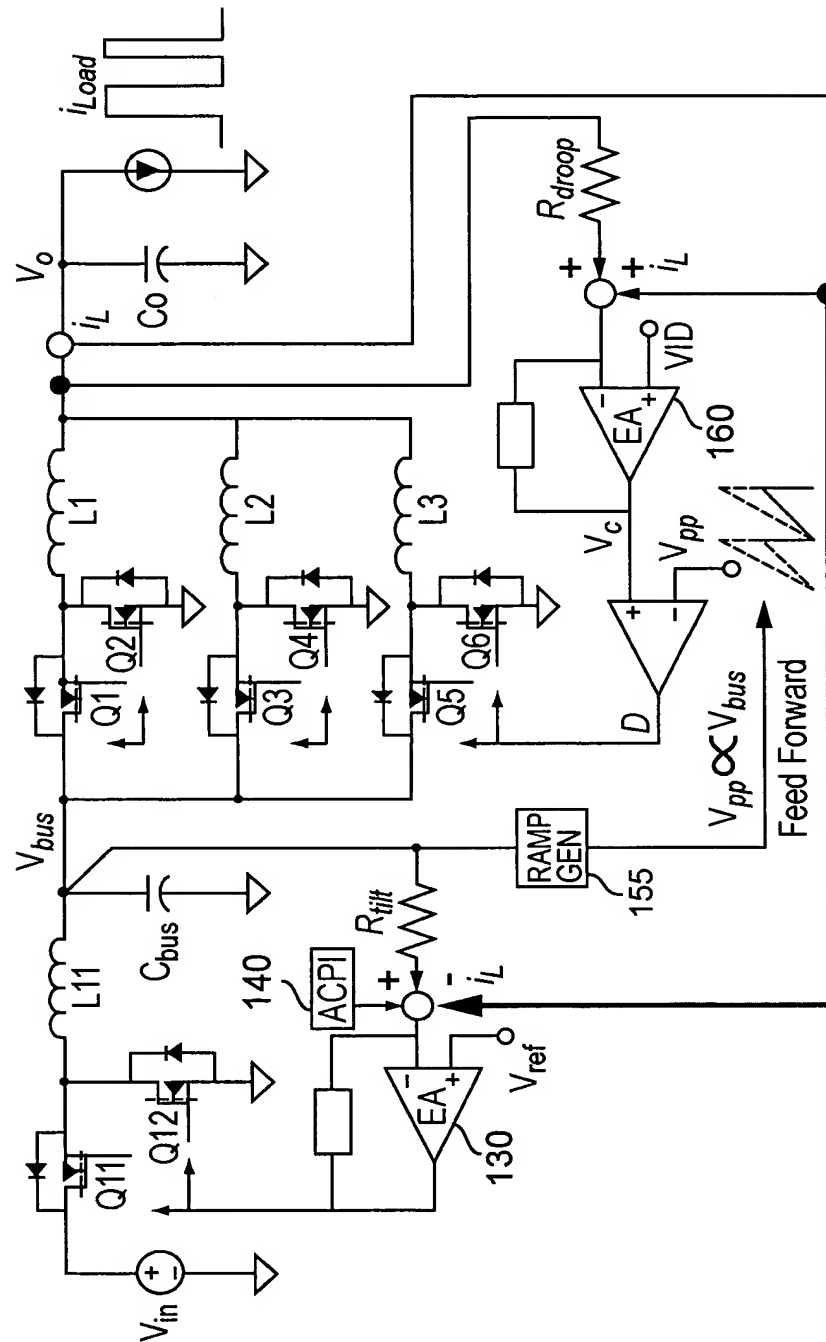


Figure 11

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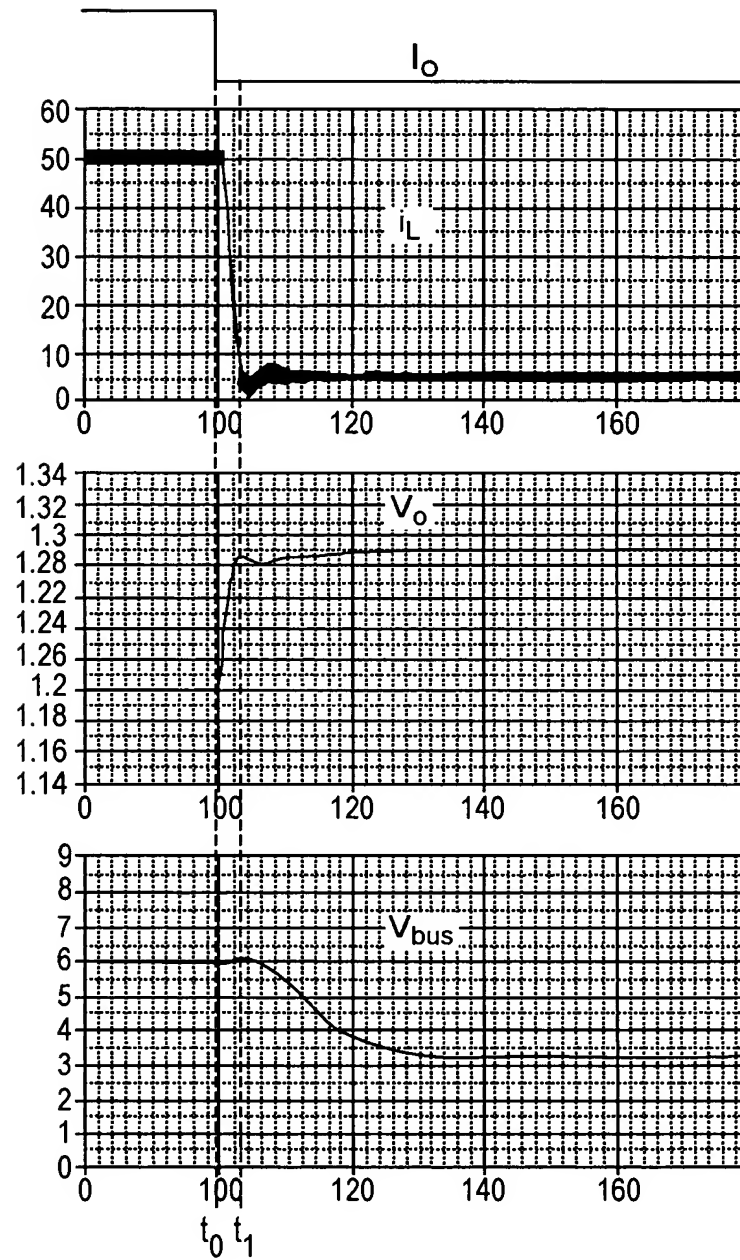
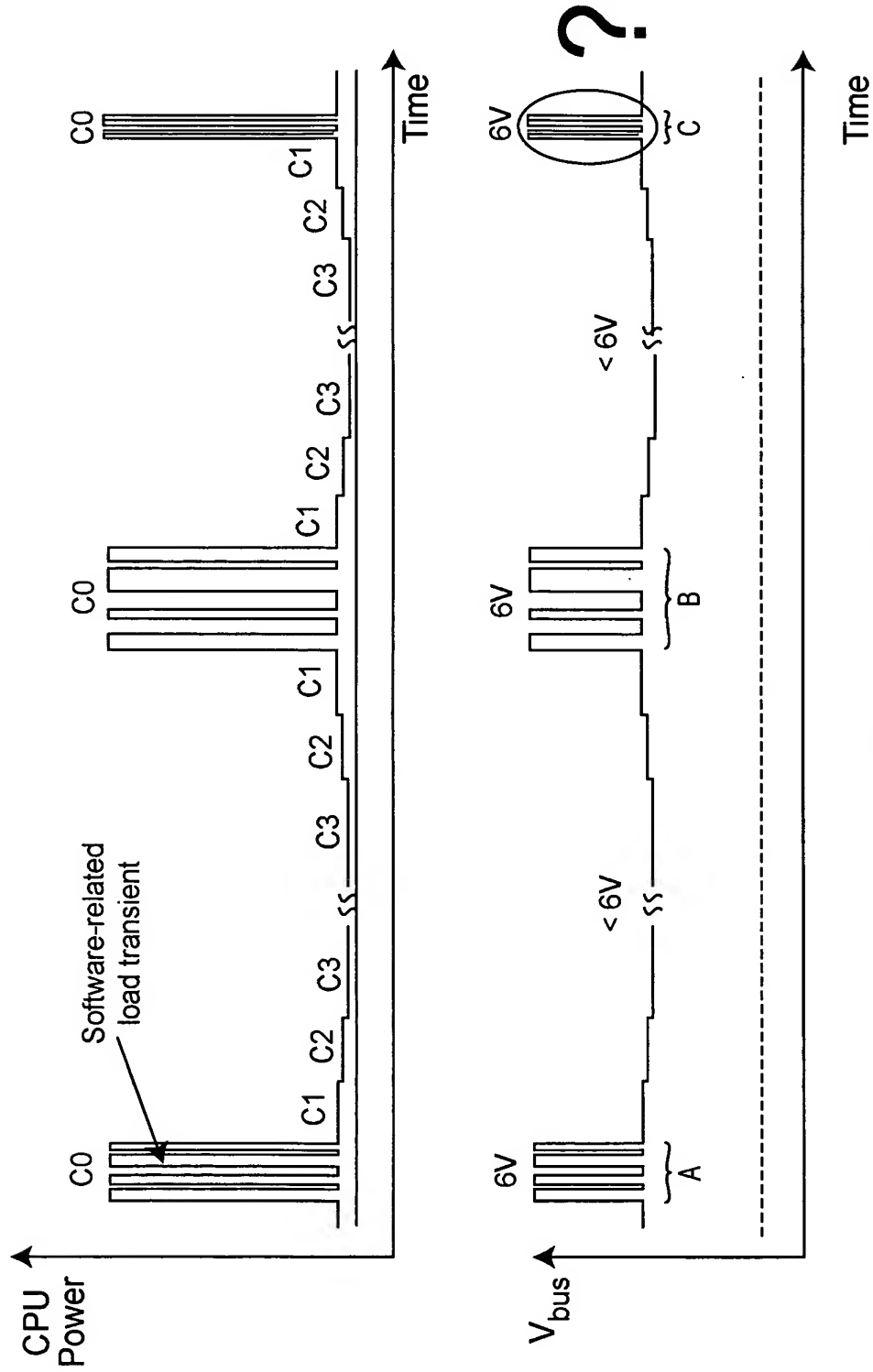


Figure 12



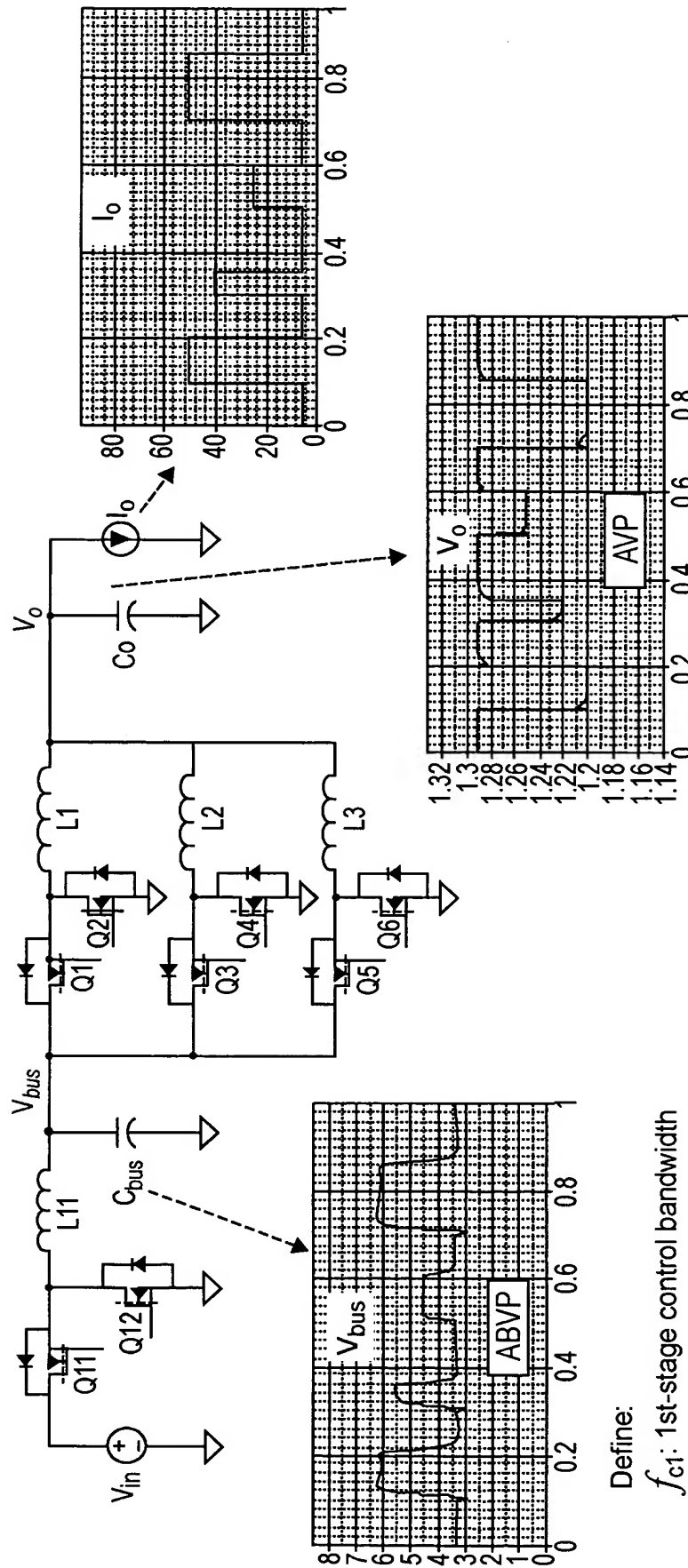


Figure 14A

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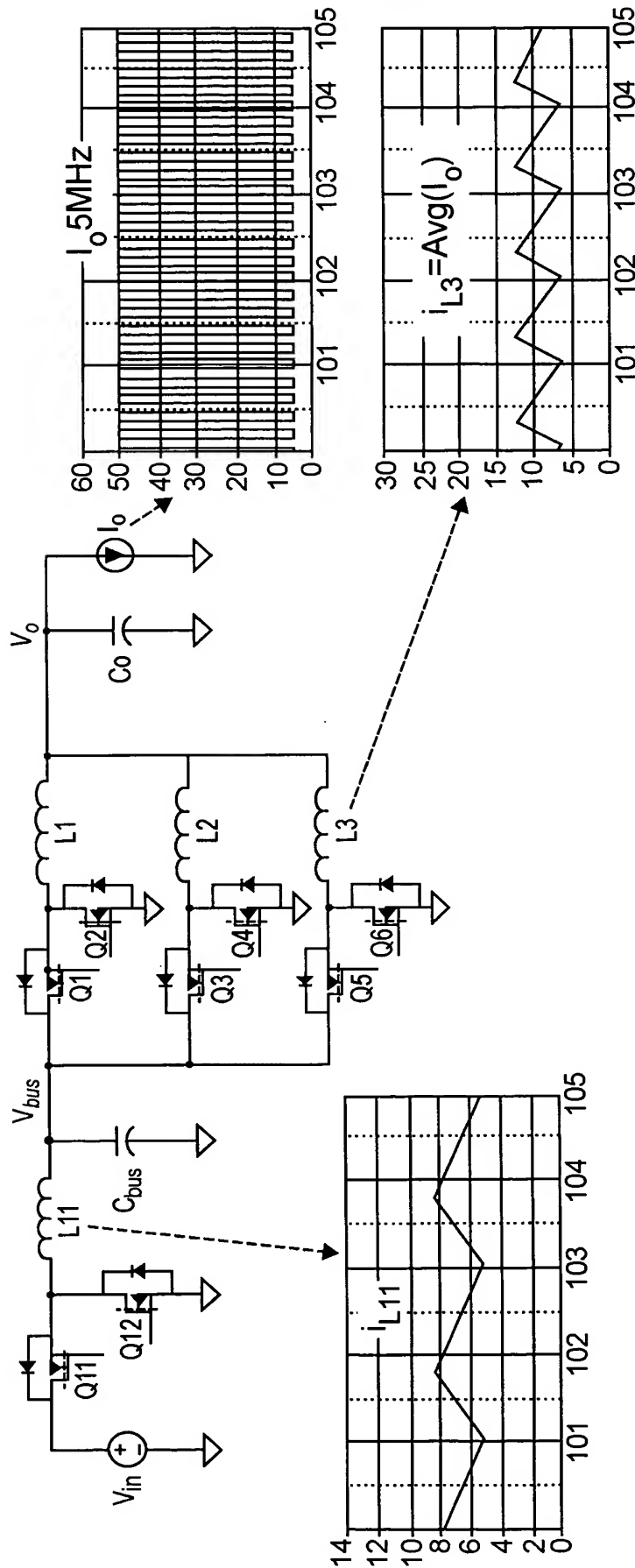


Figure 14B

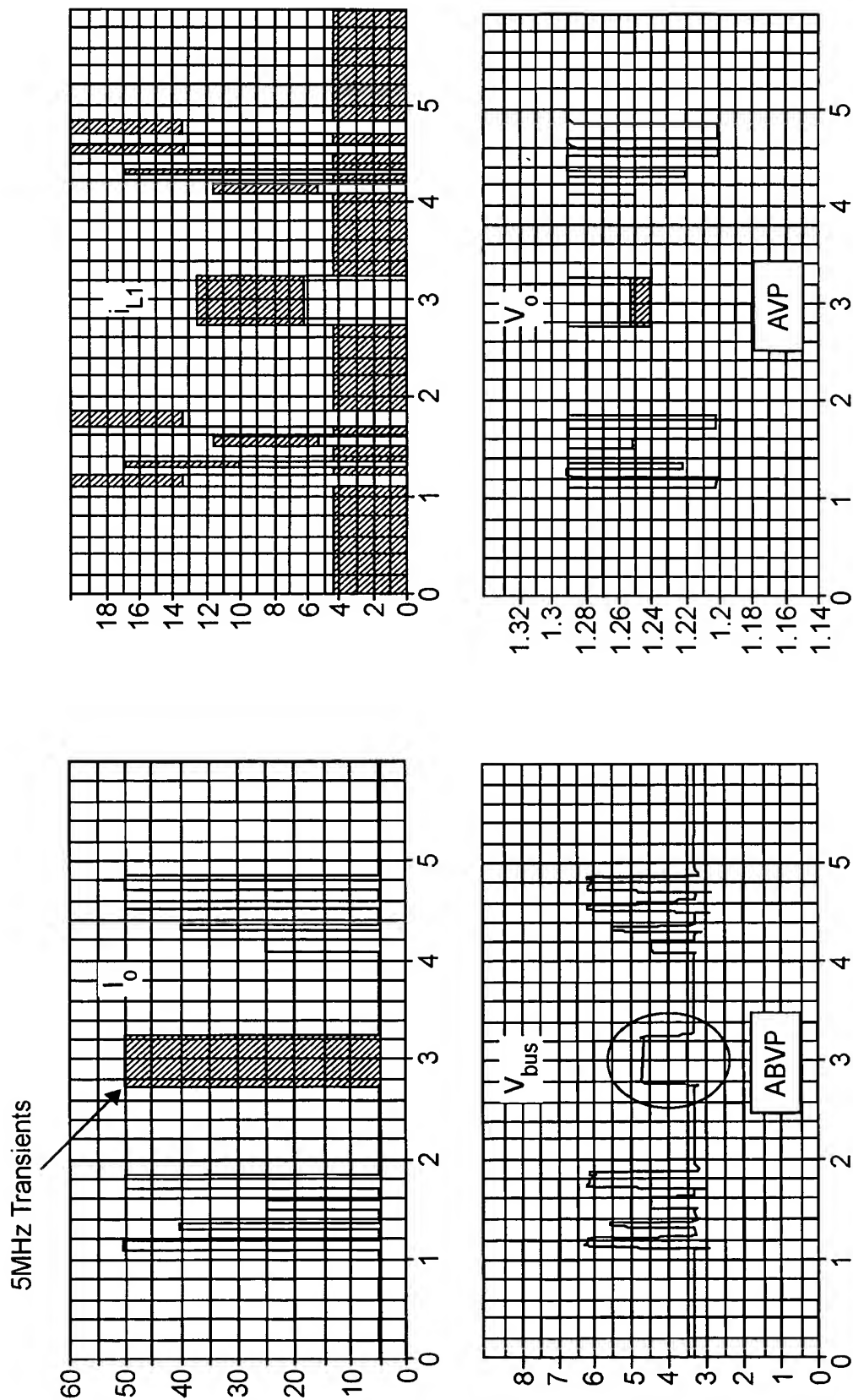


Figure 14C

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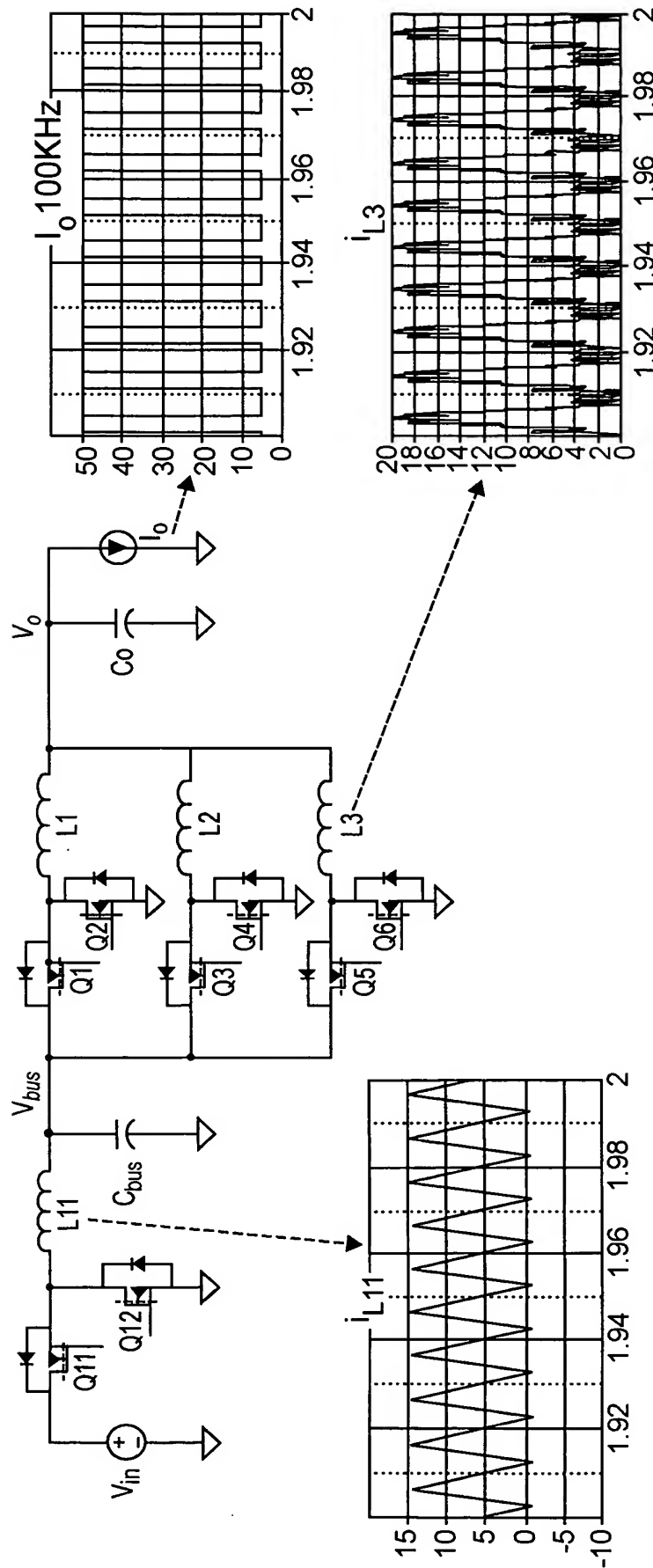


Figure 14D

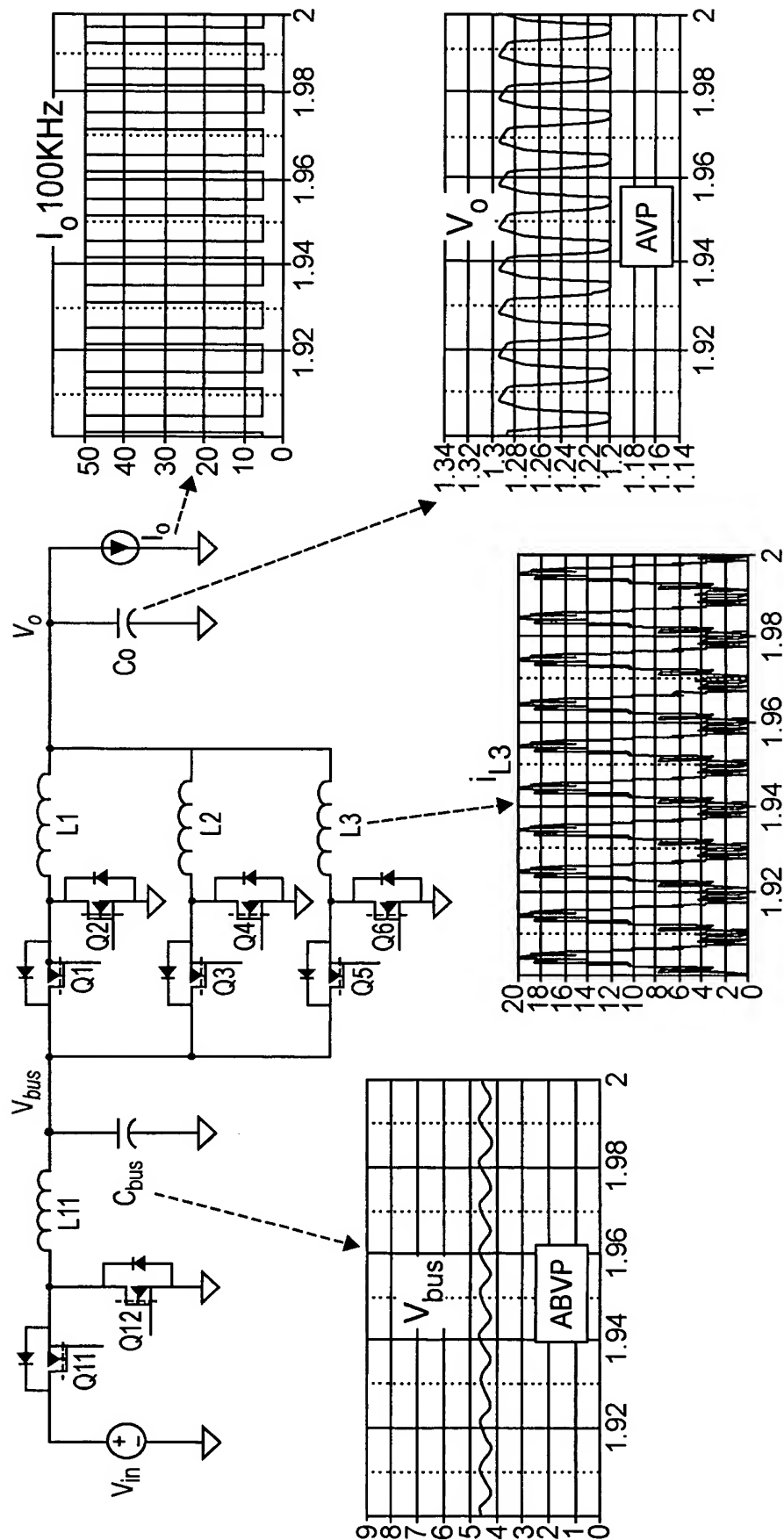


Figure 15